

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

1. (Currently amended) A core wireless engine design comprising:
a transceiver
a microprocessor; and
a standardized interface arrangement, the standardized interface arrangement adapted to be interconnected to a variety of types of host interfaces implementing a plurality of bus standards.
2. (Original) The core wireless engine design of Claim 1 wherein the core wireless engine is designed to fit into a variety of form factor units.
3. (Original) The core wireless engine design of Claim 2 wherein the core wireless engine is designed to fit within PCMCIA and Compact Flash cards.
4. (Original) A system including the core wireless engine design of Claim 1 further including a host interface interconnected to the standardized interface arrangement.

5. (Original) The system of Claim 4 wherein the core wireless engine design includes a field programmable gate array and the host interface is positioned within the field programmable gate array.

6. (Original) The core wireless engine design of Claim 1 wherein the variety of host interfaces includes a PCMCIA interface and a Compact Flash card interface.

7. (Previously Presented) The core wireless engine design of Claim 1 wherein the variety of host interfaces includes a PCMCIA interface as well as a Compact Flash interface.

8. (Previously Presented) The core wireless engine design of Claim 2 wherein the variety of form factors includes a Compact Flash form factor.

9. (Previously Presented) The core wireless engine design of Claim 2 wherein the core wireless engine is housed in a form factor that is less than 5 millimeters thick.

10. (Previously Presented) A design according to Claim 1 wherein the core wireless engine is less than 36 millimeters wide and 41 millimeters high.

11. (Currently amended) A core wireless engine design comprising:

- a transceiver
- a microprocessor; and
- a standardized interface arrangement, the standardized interface arrangement adapted to be interconnected to a variety of types of host interfaces implementing a plurality of bus standards, wherein the core wireless design is adapted to fit into a variety of form factor units.

12. (Original) The system including the core wireless design of Claim 11 wherein the system further includes a host interface.

13. (Original) The system of Claim 12 wherein the system of the core wireless engine design includes a field programmable gate array and wherein the host interface is placed within the field programmable gate array.

14. (Original) The core wireless engine design of Claim 11 wherein the standardized interface arrangement includes a standardized set of registers.

15. (Original) The core wireless engine design of Claim 11 wherein the variety of host interfaces include a PCMCIA interface as well as a Compact Flash interface.

16. (Previously Presented) The core wireless engine design of Claim 11 wherein the variety of form factors includes a Compact Flash form factor.

17. (Previously Presented) The core wireless engine design of Claim 11 wherein the core wireless engine is housed in a form factor that is less than 5 millimeters thick.

18. (Previously Presented) A design according to Claim 11 wherein the core wireless engine is less than 36 millimeters wide and 41 millimeters high.

19. (Currently Amended) A core wireless engine design comprising:

a transceiver

a microprocessor; and

a standardized interface arrangement, the standardized interface arrangement adapted to be interconnected to a variety of types of host interfaces implementing a plurality of bus standards, wherein the core wireless engine design is adapted to fit into a variety of form factor units including PCMCIA and Compact Flash cards.

20. (Original) The core wireless engine design of Claim 19 wherein the core wireless design is further adapted to fit within the form factor of a mini PCI card.

21. (Original) The core wireless engine design of Claim 19 wherein the core wireless engine is further adapted to fit within a Handspring Visor Springboard card.

22. (Original) The core wireless engine design of Claim 19 wherein the standardized interface arrangement is adapted to be interconnected to a variety of host interfaces.

23. (Original) The core wireless engine design of Claim 19 wherein the variety of host interfaces includes a PCMCIA interface as well as a Compact Flash interface.

24. (Original) The core wireless engine design of Claim 19 wherein the variety of form factors includes a Compact Flash form factor.

25. (Original) The core wireless engine design of Claim 19 wherein the standardized size is less than 5 millimeters thick.

26. (Original) The core wireless engine design of Claim 19 wherein the standardized size is less than 36 millimeters wide and 41 millimeters high.

27. (Currently amended) A method of producing a wireless modem unit, comprising:

selecting a core wireless design from a number of core wireless engine designs, each core wireless engine design having a standardized interface arrangement adapted to be interconnected to a variety of types of host interfaces implementing a plurality of bus standards, and the core wireless design adapted to fit into a variety of form factor units;

selecting a host interface and form factor unit from the variety of host interfaces and variety of form factor units and combining the selected core wireless design and selected host interface and form factor unit to produce a wireless modem unit.

28. (Original) The method of Claim 27 wherein the variety of host interfaces includes a PCMCIA interface as well as a Compact Flash interface.

29. (Original) The method of Claim 27 wherein the variety of form factors includes a Compact Flash form factor.

30. (Original) The method of Claim 27 wherein the standardized size is less than 5 millimeters thick.

31. (Original) The method of Claim 27 wherein the standardized size is less than 36 millimeters wide and 41 millimeters high.

32. (Previously Presented) The method of Claim 27 wherein the core wireless engine includes a printed circuit board that is offset from a centerline that defines the thickness of a form factor unit in which the core wireless engine design is housed.